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Atty. Docket No.: **P68987US0**  
CUSTOMER NUMBER: 00136

Honorable Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313

Sir:

Transmitted herewith for filing is the patent application in the name of:

Kwan-Yong LIM of Ichon-shi, Republic of Korea;  
Heung-Jae CHO of Ichon-shi, Republic of Korea; and  
Jung-Ho LEE of Ichon-shi, Republic of Korea,

for **STACK GATE ELECTRODE SUPPRESSED WITH INTERFACE-REACTION AND METHOD FOR FABRICATING SEMICONDUCTOR DEVICE HAVING THE SAME**. The application comprises a 19-page specification including 10 claims (2 independents) and Abstract, 7 sheets of drawings (Figs. 1-7).

Also accompanying this application for filing is:

- (1) Information Disclosure Statement, PTO-1449 form and copies of references; and
- (2) A certified copy of Korean Application No. 2002-86276, filed December 30, 2002, the priority of which is claimed under 35 U.S.C. §119.

This application is being filed under 37 C.F.R. §1.53 (without Declaration or Filing Fee). The required Declaration and Filing Fee will be filed subsequently.

Should a fee be necessary to obtain a filing date, the Commissioner is hereby authorized to charge payment of any fees set forth in §§1.17 or 1.492 during the pendency of this application, or credit any overpayment, to Deposit Account No. 06-1358. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

JACOBSON HOLMAN, PLLC

Enclosures  
YSH:kyc

By: 

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